### PATENT APPLICATION

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q83757

Tatsuo TSUJITA

Appln. No.: Based on PCT/JP03/03240

Group Art Unit: Not Yet Assigned

Confirmation No.: Not Yet Assigned

Examiner: Not Yet Assigned

Filed: September 21, 2004

For: SEMICONDUCTOR INTEGRATED CIRCUIT

# PRELIMINARY AMENDMENT

#### MAIL STOP AMENDMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Prior to examination, please amend the above-identified application as follows on the accompanying pages.

## TABLE OF CONTENTS

AMENDMENTS TO THE SPECIFICATION	2
AMENDMENTS TO THE CLAIMS	3
REMARKS	8